



Title		
Size	Number	Revision
Date:	1-Jul-2020	Sheet of
File:	EATyre_PCB_4inTyre_PCB_4in.ddb	Drawn By:

Tyre controller using PIC24FG256GA702

PIC24FJ256GA702-I/PT 28 Pin dip			PORT	PIN name	RE:Prog	ANALOG	PIN #	Application allocation		
PIN	NAME	Fullname						Purpose	Name in MCC	Group
1	MCLR	MCLR	PORTA	RA0	RP26	AN0	2	Motor Drive	MD	Digital Outputs
2	RA0	VREF+/CVREF+/AN0/C3INC/RP26/CTED1/RA0		RA1	RP27	AN1	3	Chip Select SD	CS_SD	
3	RA1	VREF-/CVREF-/AN1/C3IND/RP27/CTED2/RA1		RA2			9	Chip Select Port Exp	CS_PE	
4	RB0	PGD1/AN2/CTCMP/C2INB/RP0/RB0		RA3			10	Port Expand INT	INT_PE	Digital input
5	RB1	PGC1/AN1-/AN3/C2INA/RP1/CTED12/RB1	Port B	RA4			12	Spare		
6	RB2	AN4/C1INB/RP2/SDA2/CTED13/RB2		RB0	RP0	AN2	4	Left for PGD1		
7	RB3	AN5/C1INA/RP3/SCL2/CTED8/RB3		RB1	RP1	AN3	5	Left for PGC1		
8	Vss	VSS		RB2	RP2	AN4	6	SDI1	Default	SPI for SD card
9	RA2	OSCI/CLKI/C1IND/RA2		RB3	RP3	AN5	7	SDO1	Default	
10	RA3	OSCO/CLKO/C2IND/RA3		RB4	RP4		11	SCK1	Default	Pin 2 Terminal comms
11	RB4	SOSCI/RP4/RB4		RB5	RP5	5.5V tolerant	14	UART Receive data	Default	
12	RA4	SOSCO/PWRLCLK/RA4		RB6	RP6	5.5V tolerant	15	SDO2	Default	SPI for LCD
13	Vdd	Vdd		RB7	RP7	5.5V tolerant	16	SCK2	Default	
14	RB5	PGD3/RP5/ASDA1/OCM1E/RB5		RB8	RP8	5.5V tolerant	17	SDI3	Default	SPI for port expander
15	RB6	PGC3/RP6/ASCL1/OCM1F/RB6		RB9	RP9		18	SDO3	Default	
16	RB7	RP7/OCM1A/CTED3/INT0/RB7		RB10	RP10	5.5V tolerant	21	SCK3	Default	Pin 1 Terminal comms
17	RB8	TCK/RP8/SCL1/OCM1B/CTED10/RB8		RB11	RP11	5.5V tolerant	22	UART Transmit data	Default	
18	RB9	TDO/C1INC/C2INC/C3INC/TMPRN/RP9/SDA1/T1CK/CTED4/RB9		RB12	PR12	AN8	23	Temperature	LM45	Analog in
19	Vss	Vss		RB13	PR13	AN7	24	Pressure Sens 2	SENS2	
20	Vcap	Vcap		RB14	PR14	AN6	25	Pressure Sens 1	SENS1	
21	RB10	PGD2/TDI/RP10/OCM1C/CTED11/RB10		RB15	RP15	AN9	26	Supply Voltage in	VIN	
22	RB11	PGC2/TMS/REF1/RP11/CTED9/RB11								
23	RB12	AN8/LVDIN/RP12/RB12								
24	RB13	AN7/C1INC/RP13/OCM1D/CTPLS/RB13								
25	RB14	CVREF/AN6/C3INB/RP14/CTED5/RB14								
26	RC1	AN9/C3INA/RP15/CTED6/RB15								
27	AVss	AVSS/VSS								
28	AVdd	AVDD/VDD								

MD has to be a direct RP Pin as it's connected to the PWM module

Switch 1	PE_SW1	Digital inputs
Switch 2	PE_SW2	
Air release Valve	PE_VALVE	Digital outputs
Buzzer	PE_BUZER	
Indicator LED	PE_LED1	
LCD Chip Select	PE_LCD_CS	
LCD Data/Command	PE_LCD_DC	
LCD back light	PE_LCD_BL	Spare







